

Electronic Information Disclosure Statement

METHOD AND CIRCUIT FOR OPTIMIZING POWER CONSUMPTION AND PERFORMANCE OF DRIVER CIRCUITS

#2
21 Jan 03
R. Tall
JC474 U.S. PRO
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Application:

Confirmation:

Applicant(s): FRANCOIS ATALLAH

Docket
Number: RPS920020085






Group Art Unit:

Examiner: unassigned

search string: (6094378 or 5255224 or 5307315 or 5373479 or 5864508 or 6025751 or 5818790 or 5999461 or
5499209).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Citation No.	Patent Number	Date	Bar Code	Patentee	Class	Subclass
W	P01	6094378	2000-07-25		ZHENG	365	189.11
W	P02	5255224	1993-10-19		GALBI ET AL	365	189.09
W	P03	5307315	1994-04-26		OOWAKI ET AL	365	189.09
W	P04	5373479	1994-12-13		NODA	365	230.06
W	P05	5864508	1999-01-26		TAKASHIMA ET	365	189.11

					AL		
WU	P06	6025751	2000-02-15		CHIANG ET AL.	327	589
WU	P07	5818790	1998-10-06		KIM ET AL.	365	230.06
WU	P08	5999461	1999-12-07		VERHAEGHE ET AL.	365	189.11
WU	P09	5499209	1996-03-12		OOWAKI ET AL.	365	189.11

Remarks

(Remarks are not for responding to an office action.)

Applicants submit herewith patents, publications or other information which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR 1.56. While this Information Disclosure Statement may be "material" pursuant to 37 CFR 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such. In accordance with 37 CFR 1.97(g) the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists.

Signature

Examiner Name	Date
Huan Hoang	06/27/03